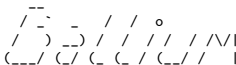


Towards the verified compilation of Lustre

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S E M I N A I R E



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Lustre [Caspi et al. (1987): "LUSTRE: A declarative language for programming synchronous systems"]



```
node count (ini, inc: int; res: bool)
```

```
  returns (n: int)
```

```
let
```

```
  n = if (true fby false) or res then ini
```

```
      else (0 fby n) + inc;
```

```
tel
```

```
val count(ini : int :: .; inc : int :: .; res : bool :: .)
```

```
returns (n : int :: .)
```

Lustre [Caspi et al. (1987): "LUSTRE: A declarative language for programming synchronous systems"]



```
node count (ini, inc: int; res: bool)
```

```
  returns (n: int)
```

```
let
```

```
  n = if (true fby false) or res then ini
      else (0 fby n) + inc;
```

```
tel
```

```
val count(ini : int :: .; inc : int :: .; res : bool :: .)
```

```
returns (n : int :: .)
```

ini	0	0	0	0	0	0	0	...
inc	0	1	2	1	2	3	0	...
res	F	F	F	F	T	F	F	...
b	T	F	F	F	F	F	F	...
c	0	0	1	3	4	0	3	...
n	0	1	3	4	0	3	3	...

- Semantic model: discrete streams.
- Nodes define a (functional) relation between input and output streams.
- Sets of 'causal' equations/definitions (always variable at left).

Lustre [Caspi et al. (1987): "LUSTRE: A declarative language for programming synchronous systems"]



```
node count (ini, inc: int; res: bool)
  returns (n: int)
```

```
let
  n = if (true fby false) or res then ini
      else (0 fby n) + inc;
```

```
tel

val count(ini : int :: .; inc : int :: .; res : bool :: .)
  returns (n : int :: .)
```

```
node COUNT (init, incr: int; reset: bool)
  returns (n: int);
let
  n = init ->
    if reset then init else pre(n) + incr;
tel;
```

Lustre [Caspi et al. (1987): "LUSTRE: A declarative language for programming synchronous systems"]



```
node count (ini, inc: int; res: bool)
```

```
  returns (n: int)
```

```
let
```

```
  n = if (true fby false) or res then ini
      else (0 fby n) + inc;
```

```
tel
```

```
val count(ini : int :: .; inc : int :: .; res : bool :: .)
```

```
returns (n : int :: .)
```

```
node count (ini, inc: int) returns (n: int)
```

```
let
```

```
  n = if (true fby false) then ini else (0 fby n) + inc;
```

```
tel
```

```
node nats (res: bool) returns (n: int)
```

```
let
```

```
  reset
```

```
    n = count(0, 1)
```

```
  every res
```

```
tel
```

Sampling and Merging

```
node avgvelocity(delta: int; sec: bool) returns (v: int)
```

```
  var r, t: int;
```

```
  let
```

```
    r = count(0, delta, false);
```

```
    t = count((1, 1, false) when sec);
```

```
    v = merge sec ((r when sec) / t) ((0 fby v) whenot sec);
```

```
  tel
```

```
val avgvelocity(delta : int :: .; sec : bool :: .) returns (v : int :: .)
```

delta	0	1	2	1	2	3	0	3	...
sec	F	F	F	T	F	T	T	F	...
r	0	1	3	4	6	9	9	12	...
(c ₁)	0	0	1	3	4	6	9	9	...
r when sec				4		9	9		...
t				1		2	3		...
(c ₂)				0		1	2		...
0 fby v	0	0	0	0	4	4	4	3	...
(0 fby v) whenot sec	0	0	0		4			3	...
v	0	0	0	4	4	4	3	3	...

Sampling and Merging

```
node avgvelocity(delta: int; sec: bool) returns (v: int)
```

```
  var r, t: int;
```

```
  let
```

```
    r = count(0, delta, false);
```

```
    t = count((1, 1, false) when sec);
```

```
    v = merge sec ((r when sec) / t) ((0 fby v) whennot sec);
```

```
  tel
```

```
val avgvelocity(delta : int :: .; sec : bool :: .) returns (v : int :: .)
```

delta	0	1	2	1	2	3	0	3	...
sec	F	F	F	T	F	T	T	F	...
r	0	1	3	4	6	9	9	12	...
(c ₁)	0	0	1	3	4	6	9	9	...
r when sec				4		9	9		...
t				1		2	3		...
(c ₂)				0		1	2		...
0 fby v	0	0	0	0	4	4	4	3	...
(0 fby v) whennot sec	0	0	0		4			3	...
v	0	0	0	4	4	4	3	3	...

Sampling and Merging

```
node avgvelocity(delta: int; sec: bool) returns (v: int)
```

```
  var r, t: int;
```

```
  let
```

```
    r = count(0, delta, false);
```

```
    t = count((1, 1, false) when sec);
```

```
    v = merge sec ((r when sec) / t) ((0 fby v) whennot sec);
```

```
  tel
```

```
val avgvelocity(delta : int :: .; sec : bool :: .) returns (v : int :: .)
```

delta	0	1	2	1	2	3	0	3	...
sec	F	F	F	T	F	T	T	F	...
r	0	1	3	4	6	9	9	12	...
(c ₁)	0	0	1	3	4	6	9	9	...
r when sec				4		9	9		...
t				1		2	3		...
(c ₂)				0		1	2		...
0 fby v	0	0	0	0	4	4	4	3	...
(0 fby v) whennot sec	0	0	0		4			3	...
v	0	0	0	4	4	4	3	3	...

Sampling and Merging

```
node avgvelocity(delta: int; sec: bool) returns (v: int)
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```
  var r, t: int;
```

```
  let
```

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    r = count(0, delta, false);
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```
    t = count((1, 1, false) when sec);
```

```
    v = merge sec ((r when sec) / t) ((0 fby v) whenot sec);
```

```
  tel
```

```
val avgvelocity(delta : int :: .; sec : bool :: .) returns (v : int :: .)
```

delta	0	1	2	1	2	3	0	3	...
sec	F	F	F	T	F	T	T	F	...
r	0	1	3	4	6	9	9	12	...
(c ₁)	0	0	1	3	4	6	9	9	...
r when sec				4		9	9		...
t				1		2	3		...
(c ₂)				0		1	2		...
0 fby v	0	0	0	0	4	4	4	3	...
(0 fby v) whenot sec	0	0	0		4			3	...
v	0	0	0	4	4	4	3	3	...

Sampling and Merging

```
node avgvelocity(delta: int; sec: bool) returns (v: int)
```

```
  var r, t: int;
```

```
  let
```

```
    r = count(0, delta, false);
```

```
    t = count((1, 1, false) when sec);
```

```
    v = merge sec ((r when sec) / t) ((0 fby v) whenot sec);
```

```
  tel
```

```
val avgvelocity(delta : int :: .; sec : bool :: .) returns (v : int :: .)
```

delta	0	1	2	1	2	3	0	3	...
sec	F	F	F	T	F	T	T	F	...
r	0	1	3	4	6	9	9	12	...
(c ₁)	0	0	1	3	4	6	9	9	...
r when sec				4		9	9		...
t				1		2	3		...
(c ₂)				0		1	2		...
0 fby v	0	0	0	0	4	4	4	3	...
(0 fby v) whenot sec	0	0	0		4			3	...
v	0	0	0	4	4	4	3	3	...

Sampling and Merging

```
node avgvelocity(delta: int; sec: bool) returns (v: int)
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```
  var r, t: int;
```

```
  let
```

```
    r = count(0, delta, false);
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```
    t = count((1, 1, false) when sec);
```

```
    v = merge sec ((r when sec) / t) ((0 fby v) whenot sec);
```

```
  tel
```

```
val avgvelocity(delta : int :: .; sec : bool :: .) returns (v : int :: .)
```

delta	0	1	2	1	2	3	0	3	...
sec	F	F	F	T	F	T	T	F	...
r	0	1	3	4	6	9	9	12	...
(c ₁)	0	0	1	3	4	6	9	9	...
r when sec				4		9	9		...
t				1		2	3		...
(c ₂)				0		1	2		...
0 fby v	0	0	0	0	4	4	4	3	...
(0 fby v) whenot sec	0	0	0		4			3	...
v	0	0	0	4	4	4	3	3	...

Static Clocking

```
node avgvelocity(delta: int; sec: bool) returns (v: int)
```

```
  var r, t: int;
```

```
  let
```

```
    r = count(0, delta, false);
```

```
    t = count((1, 1, false) when sec);
```

```
    v = merge sec ((r when sec) / t) ((0 fby v) whenot sec);
```

```
  tel
```

```
val avgvelocity(delta : int :: .; sec : bool :: .) returns (v : int :: .)
```

sec	base	F	F	F	T	F	T	...
r	base	0	1	3	4	6	9	...
t	base on (sec = T)				1		2	...
(0 fby v) whenot sec	base on (sec = F)	0	0	0		4		...
v	base	0	0	0	4	4	4	...

Static Clocking

```
node avgvelocity(delta: int; sec: bool) returns (v: int)
```

```
  var r, t: int;
```

```
  let
```

```
    r = count(0, delta, false);
```

```
    t = count((1, 1, false) when sec);
```

```
    v = merge sec ((r when sec) / t) ((0 fby v) whenot sec);
```

```
  tel
```

```
val avgvelocity(delta : int :: .; sec : bool :: .) returns (v : int :: .)
```

sec	base	F	F	F	T	F	T	...
r	base	0	1	3	4	6	9	...
t	base on (sec = T)				1		2	...
(0 fby v) whenot sec	base on (sec = F)	0	0	0		4		...
v	base	0	0	0	4	4	4	...

$$\frac{C \vdash e :: ck \quad C \vdash x :: ck}{C \vdash e \text{ when } x :: ck \text{ on } (x = T)}$$

$$\frac{C \vdash x :: ck \quad C \vdash e_t :: ck \text{ on } (x = T) \quad C \vdash e_f :: ck \text{ on } (x = F)}{C \vdash \text{merge } x \ e_t \ e_f :: ck}$$

Static Clocking

```
node avgvelocity(delta: int; sec: bool) returns (v: int)
```

```
  var r, t: int;
```

```
  let
```

```
    r = count(0, delta, false);
```

```
    t = count((1, 1, false) when sec);
```

```
    v = merge sec ((r when sec) / t) ((0 fby v) whenot sec);
```

```
  tel
```

```
val avgvelocity(delta : int :: .; sec : bool :: .) returns (v : int :: .)
```

sec	base	F	F	F	T	F	T	...
r	base	0	1	3	4	6	9	...
t	base on (sec = T)				1		2	...
(0 fby v) whenot sec	base on (sec = F)	0	0	0		4		...
v	base	0	0	0	4	4	4	...

- Static inference/verification of clocking.
- “Clocks in the source language are transformed into control structures in the target language.” [Biernacki et al. (2008): “Clock-directed modular code generation for synchronous data-flow languages”]

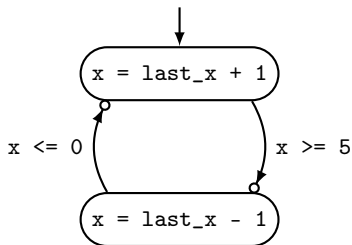
Sampling and merging: what for?

- Provide a means of conditional activation.
- Programming directly with them can be tricky.
- Serve as a target for more complicated structures.

```
node main (go : bool)
  returns (x : int)
  var last_x : int;
let
  last_x = 0 fby x;

  automaton
  state Up
    do x = last_x + 1
    until x >= 5 then Down

  state Down
    do x = last_x - 1
    until x <= 0 then Up
  end;
tel
```



Sampling and merging: what for?

- Provide a means of conditional activation.
- Programming directly with them can be tricky.
- Serve as a target for more complicated structures.

```
node main (go : bool)
```

```
  returns (x : int)
```

```
  var last_x : int;
```

```
let
```

```
  last_x = 0 fby x;
```

```
type st = St_Up | St_Down
```

```
(* ... *)
```

```
last_x = 0 fby x
```

```
automaton
```

```
state Up
```

```
  do x = last_x + 1
```

```
  until x >= 5 then Down
```

```
x_St_Down = (last_x when St_Down(ck)) - 1
```

```
x_St_Up = (last_x when St_Up(ck)) + 1
```

```
x = merge ck (St_Down: x_St_Down)  
              (St_Up: x_St_Up);
```

```
state Down
```

```
  do x = last_x - 1
```

```
  until x <= 0 then Up
```

```
ck = St_Up fby ns
```

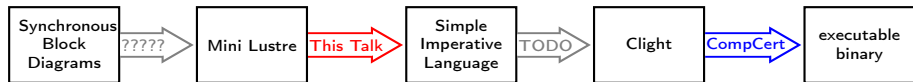
```
ns = ...
```

```
end;
```

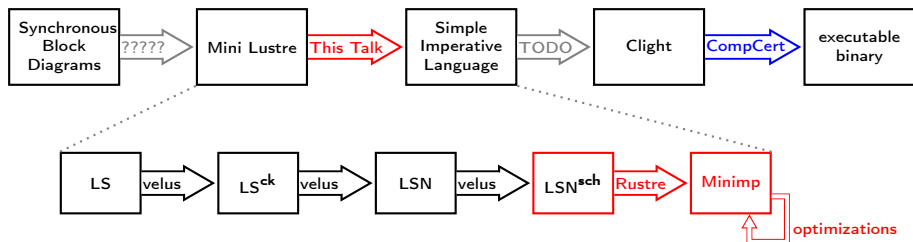
```
tel
```

[Colaço, Pagano, and Pouzet (2005): "A Conservative Extension of Synchronous Data-flow with State Machines"]

Verifying Lustre compilation in Coq



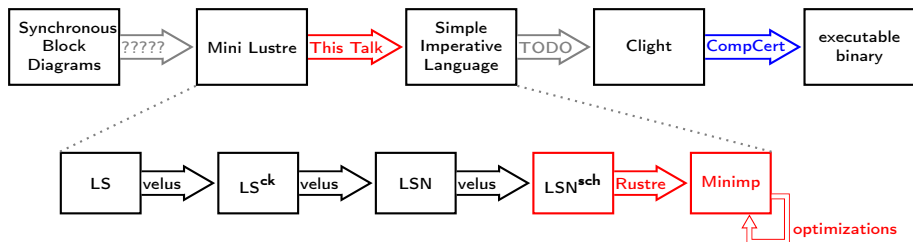
Verifying Lustre compilation in Coq



The Velus project (2008–2010, 2010–2013)

- Pouzet, Hamon, Auger, and others. [Auger (2013): "Compilation certifiée de SCADE/LUSTRE"]
- Much formalized in Coq, some pen-and-paper proofs.
- Succession of source-to-source passes.
- Streams modelled as lists.

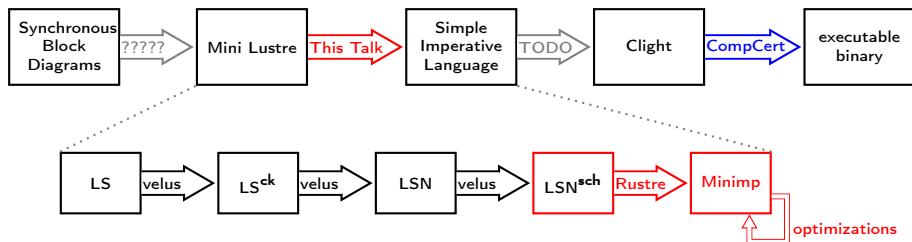
Verifying Lustre compilation in Coq



The Rustre project (2015–)

- *A bit less*: tupling ~~X~~, modular resets ~~X~~, n-ary merge ~~X~~
- *A bit more*: imperative code generation and optimization in Coq.
- *A bit different*: streams as functions from nats to values.
- Complete: code generation and optimization.
- Ongoing:
 - refine semantic model / proof of existence
 - incorporate Velus passes into a complete tool chain
 - integrate with CompCert...

Verifying Lustre compilation in Coq



Integration with CompCert

[Blazy, Dargaye, and Leroy (2006): "Formal Verification of a C Compiler Front-End"]

[Leroy (2009): "Formal verification of a realistic compiler"]

[Bedin França et al. (2011): "Towards Formally Verified Optimizing Compilation in Flight Control Software"]

- Incorporate types and operations from CompCert into Lustre.
- Generate Clight from Minimp and show correctness.
- Part of ITEA 3 14014 ASSUME Project.
- Summer internship of L elio Brun.
- What about external functions? external nodes? external types?

Normalization [Auger (2013): "Compilation certifiée de SCADE/LUSTRE"]

```
node avgvelocity(delta: int; sec: bool)
```

```
  returns (v: int)
```

```
  var r, t: int;
```

```
let
```

```
  r = count(0, delta, false);
```

```
  t = count((1, 1, false) when sec);
```

```
  v = merge sec ((r when sec) / t  
                ((0 fby v) whenot sec));
```

```
tel
```

normalize 

```
node avgvelocity(delta: int; sec: bool)
```

```
  returns (v: int)
```

```
  var r, t, w: int;
```

```
let
```

```
  w = 0 fby v;
```

```
  r = count(0, delta, false);
```

```
  t = count((1, 1, false) when sec);
```

```
  v = merge sec ((r when sec) / t  
                (w whenot sec));
```

```
tel
```

- Rewrite to give each `fby` and node instantiation its own equation.
- Group `merges` at tops of equations.
- Introduce fresh variables; exploit referential transparency.
- Proof by validation
 - External program produces $w = e$; eqs2.
 - Coq validator checks that substituting $w = e$ into eqs2 gives eqs.

```
node avgvelocity(delta: int; sec: bool)
  returns (v: int)
  var r, t, w: int;
let
  w = 0 fby v;
  r = count(0, delta, false);
  t = count((1, 1, false) when sec);
  v = merge sec ((r when sec) / t)
              (w whenot sec);
tel
```



```
node avgvelocity(delta: int; sec: bool)
  returns (v: int)
  var r, t, w: int;
let
  r = count(0, delta, false);
  t = count((1, 1, false) when sec);
  v = merge sec ((r when sec) / t)
              (w whenot sec);
  w = 0 fby v;
tel
```

- Equations semantics are independent of order; but correct compilation will depend on order.
- Rewrite to define **most variables before they are read**,
... and **fby variables after they are read**; optimize adjacencies.
- Proof by validation
 - External program generates a sequence of permutations.
 - Verified Coq function applies them successively.

Outline

A simple program in Lustre

Verifying Lustre compilation in Coq

Dataflow language: syntax and semantics

Imperative language: syntax and semantics

Relating the Dataflow and Imperative models

Optimization of control structures

Conclusion

Rustre: dataflow language

Expressions

$e ::=$	x	variable
	k	constant
	$e \oplus e$	operator
	$e \text{ when } (x = k)$	sub-sampling
$ce ::=$	$\text{merge } x \ ce_t \ ce_f$	binary merge
	e	non-control expression

Equations

$eq ::=$	$x = (ce)^{ck}$
	$x = (k_0 \text{ fby } e)^{ck}$
	$x = (f \ e)^{ck}$

(Scheduled) Nodes

node $f (x : \tau)$ **returns** $(x : \tau)$
var $x : \tau, \dots, x : \tau$
let $eq; \dots; eq \text{ tel}$

Clocks

$ck ::=$	base
	$ck \text{ on } (x = k)$


```
Inductive clock : Set :=  
| Cbase : clock  
| Con : clock → ident → bool → clock.
```

```
Inductive lexp : Type :=  
| Econst : const → lexp  
| Evar : ident → lexp  
| Ewhen : lexp → ident → bool → lexp.
```

```
Inductive laexp : Type :=  
| LAexp : clock → lexp → laexp.
```

```
Inductive cexp : Type :=  
| Emerge : ident → cexp → cexp → cexp  
| Eexp : lexp → cexp.
```

```
Inductive caexp : Type :=  
| CAexp : clock → cexp → caexp.
```

```
Inductive equation : Type :=  
| EqDef : ident → caexp → equation  
| EqApp : ident → ident → laexp → equation  
| EqFby : ident → const → laexp → equation.
```

Semantics: Dataflow models

n	1	2	3	4	5	6	7	...	base
ck	F	F	T	F	T	T	F	...	base
x = n when ck			3		5	6		...	base on (ck = T)
y = 0 fby x			0		3	5		...	base on (ck = T)
z = 9 whenot ck	9	9		9			9	...	base on (ck = F)
w = merge ck y z	9	9	0	9	3	5	9	...	base

- Model absence

Inductive value := absent | present (v : const).

[Boulmé and Hamon (2001): *A clocked denotational semantics for Lucid-Synchrone in Coq*]

[Paulin-Mohring (2009): "A constructive denotational semantics for Kahn networks in Coq"]

- Lists: $1 :: (2 :: (3 :: (4 :: [])))$ or $(((\epsilon \cdot 1) \cdot 2) \cdot 3) \cdot 4$

- Coinductive streams?

- Functions from natural numbers to values

Notation stream A := (nat → A).

Rustre: semantic model

Definition `global` := list node.

Definition `history` := Coq.FSets.FMapPositive.PositiveMap.t (stream value).

Inductive `sem_equation` (G: global)
: history → equation → Prop :=

| (SEqDef:)
sem_var H x xs →
sem_cexp H ce xs →

sem_equation G H (EqDef x ck ce)
x = (ce)^{ck}

| (SEqApp:)
sem_lexp H le ls →
sem_var H x xs →
sem_node G f ls xs →

sem_equation G H (EqApp x ck f le)
x = (f le)^{ck}

| (SEqFby:)
sem_lexp H le ls →
sem_var H x xs →
xs = fby v0 ls → x = (v0 fby le)^{ck}

sem_equation G H (EqFby x ck v0 le)

with `sem_node` (G: global)
: ident
→ stream value
→ stream value
→ Prop :=

| (SNode:)
find_node f G
= Some (mk_node f i o eqs) →
(∃ H, sem_var H i xs
∧ sem_var H o ys
∧ ...
∧ Forall (sem_equation G H) eqs)

→ sem_node G f xs ys.

Rustre: semantic model

Definition global := list node.

Definition history := Coq.FSets.FMapPositive.PositiveMap.t (stream value).

Inductive sem_equation (G: global)
: history → equation → Prop :=

| (SEqDef:)
sem_var H x xs →
sem_cexp H ce xs →

sem_equation G H (EqDef x ck ce)
x = (ce)^{ck}

| (SEqApp:)
sem_lexp H le ls →
sem_var H x xs →
sem_node G f ls xs →

sem_equation G H (EqApp x ck f le)
x = (f le)^{ck}

| (SEqFby:)
sem_lexp H le ls →
sem_var H x xs →
xs = fby v0 ls → x = (v0 fby le)^{ck}

sem_equation G H (EqFby x ck v0 le)



f : stream(T) → stream(T')
with sem_node (G: global)

: ident
→ stream value
→ stream value
→ Prop :=

| (SNode:)
find_node f G
= Some (mk_node f i o eqs) →
(∃ H, sem_var H i xs
∧ sem_var H o ys
∧ ...
∧ Forall (sem_equation G H) eqs)

→ sem_node G f xs ys.

Rustre: semantic model: fby

$$\begin{aligned}\text{fby}_{v_0}^\#(v.s) &= v_0.\text{fby}_v^\#(s) \\ \text{fby}_{v_0}^\#(\text{abs}.s) &= \text{abs}.\text{fby}_{v_0}^\#(s) \\ \text{fby}_{v_0}^\#(\epsilon) &= \epsilon\end{aligned}$$

```
Fixpoint hold (v0: const) (xs: stream value) (n: nat) : const :=
  match n with
  | 0 => v0
  | S m => match xs m with
           | absent => hold v0 xs m
           | present hv => hv
         end
  end.
```

```
Definition fby (v0: const) (xs: stream value) (n: nat) : value :=
  match xs n with
  | absent => absent
  | _ => present (hold v0 xs n)
  end.
```

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A simple program in Lustre

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Dataflow language: syntax and semantics

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Minimp: imperative language

$c ::=$	x	variable
	$\text{mem}(x)$	memory
	k	constant
	$e \oplus e$	operator
$s ::=$	$x := c$	variable assignment
	$\text{mem}(x) := c$	memory assignment
	$x := f.\text{step } o(x)$	node transition assignment (and update)
	$f.\text{reset } o$	initialize node memory
	$\text{if } c \{s\} \text{ else } \{s\}$	conditional branching
	$s; s$	sequential composition
	skip	nop

Generation of imperative code [Biernacki et al. (2008): "Clock-directed modular code generation for synchronous data-flow languages"]

```
node avgvelocity(delta: int; sec: bool)
  returns (v: int)
  var r, t, w: int;
let
  r = count(0, delta, false);
  t = count((1, 1, false) when sec);
  v = merge sec ((r when sec) / t)
            (w whennot sec);
  w = 0 fby v;
tel
```

```
memory w;
instance o1, o2;

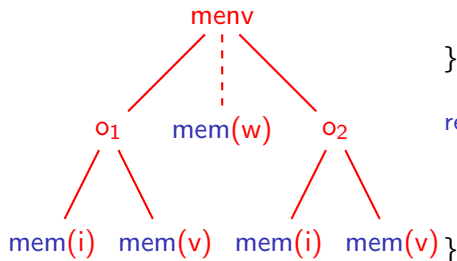
step(delta: int, sec: bool) returns (v: int) {
  var r, t : int;

  r := count.step o1 (0, delta, false);
  if sec { t := count.step o2 (1, 1, false) };
  if sec { v := r / t }
  else { v := mem(w) };
  mem(w) := v
}

reset() returns () {
  count.reset o1;
  count.reset o2;
  mem(w) := 0
}
```


Generation of imperative code [Biernacki et al. (2008): "Clock-directed modular code generation for synchronous data-flow languages"]

```
node avgvelocity(delta: int; sec: bool)
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              (w whennot sec);
  w = 0 fby v;
tel
```



```
memory w;
instance o1, o2;
```

```
step(delta: int, sec: bool) returns (v: int) {
  var r, t: int;

  r := count.step o1 (0, delta, false);
  if sec { t := count.step o2 (1, 1, false) };
  if sec { v := r / t }
  else { v := mem(w) };
  mem(w) := v
}
```

```
reset() returns () {
  count.reset o1;
  count.reset o2;
  mem(w) := 0
}
```

Minimp: semantic model

Inductive stmt_eval :

program \rightarrow heap \rightarrow stack \rightarrow stmt \rightarrow heap * stack \rightarrow Prop :=

| Iassign:

exp_eval memv env e v \rightarrow

PM.add x v env = env' \rightarrow

stmt_eval prog memv env (Assign x e) (memv, env')

$x := e$

| Iassignst:

exp_eval memv env e v \rightarrow

madd_mem x v memv = memv' \rightarrow

stmt_eval prog memv env (AssignSt x e) (memv', env)

| Istep:

$\text{mem}(x) := e$

exp_eval memv env e v \rightarrow

mfind_inst o memv = Some(omenv) \rightarrow

stmt_step_eval prog omenv fcls v omenv' rv \rightarrow

madd_obj o omenv' memv = memv' \rightarrow

PM.add x rv env = env' \rightarrow

stmt_eval prog memv env (Step_ap x fcls o e) (memv', env')

:

$x := \text{fcls.step } o \text{ (e)}$

Minimp: semantic model

Inductive stmt_eval :

program → heap → stack → stmt → heap * stack → Prop :=

| Iassign:

exp_eval memv env e v →

PM.add x v env = env' →

stmt_eval prog memv env (Assign x e) (memv, env')

$x := e$

| Iassignst:

exp_eval memv env e v →

madd_mem x v memv = memv' →

stmt_eval prog memv env (AssignSt x e) (memv', env)

$\text{mem}(x) := e$

| Istep:

exp_eval memv env e v →

mfind_inst o memv = Some(omenv) →

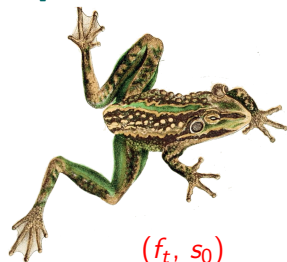
stmt_step_eval prog omenv fcls v omenv' rv →

madd_obj o omenv' memv = memv' →

PM.add x rv env = env' →

stmt_eval prog memv env (Step_ap x fcls o e) (memv', env')

$x := \text{fcls.step } o \text{ (e)}$



(f_t, s_0)

$S \times T \rightarrow T' \times S$

S

:

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Is well scheduled

Variables (mems : PS.t) (arg: Nelist.nelist ident).

Inductive Is_well_sch : list equation → Prop :=

| WSchNil: Is_well_sch []

| WSchEq:

Is_well_sch eqs →

($\forall i, \text{Is_free_in_eq } i \text{ eq} \rightarrow$

($\neg \text{PS.In } i \text{ mems} \rightarrow \text{Is_variable_in } i \text{ eqs}$
 $\vee \text{Nelist.In } i \text{ arg}$)

$\wedge (\text{PS.In } i \text{ mems} \rightarrow \neg \text{Is_defined_in } i \text{ eqs})) \rightarrow$

($\forall i, \text{Is_defined_in_eq } i \text{ eq} \rightarrow \neg \text{Is_defined_in } i \text{ eqs}$) \rightarrow

Is_well_sch (eq :: eqs).

| ...

alleqs

[

[$\dots ; w = v_0 \text{ fby } e ; \dots$] ++ ($x = e :: [\dots ; y = e ; \dots]$) input

Is well scheduled

Variables (mems : PS.t) (arg: Nelist.nelist ident).

Inductive Is_well_sch : list equation → Prop :=

| WSchNil: Is_well_sch []

| WSchEq:

Is_well_sch eqs →

(∀ i, Is_free_in_eq i eq →

(¬PS.In i mems → Is_variable_in i eqs
∨ Nelist.In i arg)

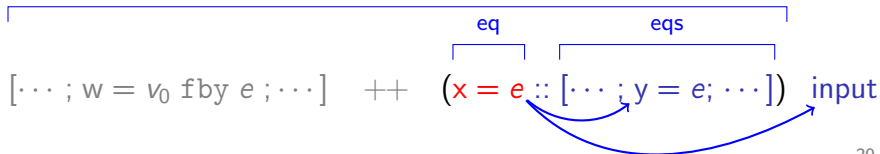
∧ (PS.In i mems → ¬Is_defined_in i eqs)) →

(∀ i, Is_defined_in_eq i eq → ¬Is_defined_in i eqs) →

Is_well_sch (eq :: eqs).

| ...

alleqs



Is well scheduled

Variables (mems : PS.t) (arg: Nelist.nelist ident).

Inductive Is_well_sch : list equation → Prop :=

| WSchNil: Is_well_sch []

| WSchEq:

Is_well_sch eqs →

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($\neg \text{PS.In } i \text{ mems} \rightarrow \text{Is_variable_in } i \text{ eqs}$
 $\vee \text{Nelist.In } i \text{ arg}$)

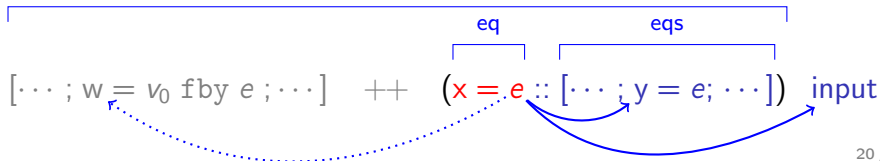
$\wedge (\text{PS.In } i \text{ mems} \rightarrow \neg \text{Is_defined_in } i \text{ eqs})) \rightarrow$

($\forall i, \text{Is_defined_in_eq } i \text{ eq} \rightarrow \neg \text{Is_defined_in } i \text{ eqs}$) →

Is_well_sch (eq :: eqs).

| ...

alleqs



Translation: definition

Variable mems : PS.t.

Definition tovar (x: ident) : exp := if PS.mem x mems then State x else Var x.

Fixpoint Control (ck: clock) (s: stmt) : stmt :=

```
match ck with
| Cbase ⇒ s
| Con ck x true ⇒ Control ck (Ifte (tovar x) s Skip)
| Con ck x false ⇒ Control ck (Ifte (tovar x) Skip s)
end.
```

Fixpoint translate_cexp (x: ident) (e : cexp) {struct e} : stmt :=

```
match e with
| Emerge y t f ⇒ Ifte (tovar y) (translate_cexp x t) (translate_cexp x f)
| Eexp l ⇒ Assign x (translate_lexp l)
end.
```

Definition translate_eqn (eqn: equation) : stmt :=

```
match eqn with
| EqDef x (CAexp ck ce) ⇒ Control ck (translate_cexp x ce)
| EqApp x f (LAexp ck le) ⇒ Control ck (Step_ap x f x (translate_lexp le))
| EqFby x v (LAexp ck le) ⇒ Control ck (AssignSt x (translate_lexp le))
end.
```


Translation: definition

Variable mems : PS.t.

Definition tovar (x: ident) : exp := if PS.mem x mems then State x else Var x.

Fixpoint Control (ck: clock) (s: stmt) : stmt :=

```
match ck with
| Cbase ⇒ s
| Con ck x true ⇒ Control ck (Ifte (tovar x) s Skip)
| Con ck x false ⇒ Control ck (Ifte (tovar x) Skip s)
end.
```

Fixpoint translate_cexp (x: ident)(e : cexp) {struct e} : stmt := ...

Definition translate_eqn (eqn: equation) : stmt :=

```
match eqn with
| EqDef x (CAexp ck ce) ⇒ Control ck (translate_cexp x ce)
| EqApp x f (LAexp ck le) ⇒ Control ck (Step_ap x f x (translate_lexp le))
| EqFby x v (LAexp ck le) ⇒ Control ck (AssignSt x (translate_lexp le))
end.
```

Definition translate_eqns (eqns: list equation): stmt :=

```
List.fold_left (fun i eq ⇒ Comp (translate_eqn eq) i) eqns Skip.
```

Correctness theorem

Variables (G : global)
 (Hwdef : Welldef_global G).



Theorem is_event_loop_correct:

$\text{sem_node } G \text{ main } xss \text{ } ys \rightarrow \leftarrow \text{CO}_0 \cdot \text{CO}_1 \cdot \text{CO}_2 \cdots = f(xss_0 \cdot xss_1 \cdot xss_2 \cdots)$

$\forall n, \exists \text{menv env},$

step (S n) (translate G) r main obj css menv env

$\wedge (\forall \text{co}, \text{ys } n \not\vdash \text{present co} \leftrightarrow \text{PM.find r env} = \text{Some co}).$

clock-directed translation

Fixpoint step (n: nat) P r main obj css menv' env': Prop :=

match n with

| 0 \Rightarrow stmt_eval P hempty sempty (Reset_ap main obj) (menv', env')

| S n \Rightarrow let xss := Nelist.map Const (css n) in

\exists menvN envN,

step n P r main obj css menv env

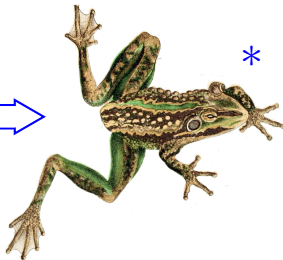
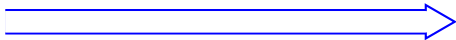
\wedge stmt_eval P menv env (Step_ap r main obj xss) (menv', env')

end.



f.reset obj;
 repeat (n + 1) {
 r := f.step obj (css n)
 }

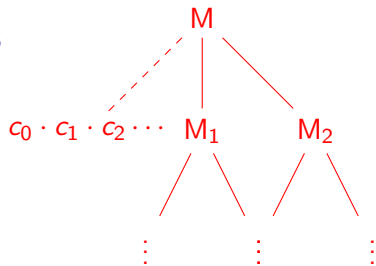
Induction on n : (internal) memories



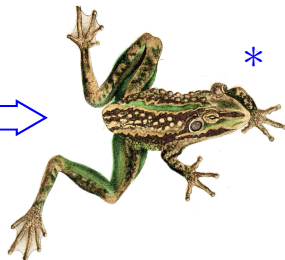
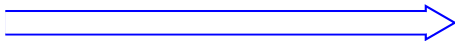
`sem_node G f xs ys`

`stmt_eval (translate G) memv env (r := f.step obj (ci)) (memv', env')`

Induction on n : (internal) memories



$\exists M, \text{msem_node } G \text{ f xs } M \text{ ys}$



$\text{sem_node } G \text{ f xs ys}$

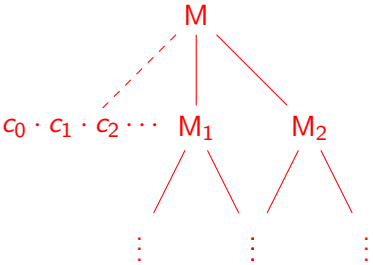
$\text{stmt_eval (translate } G) \text{ memv env (r := f.step obj (ci)) (memv', env')}$

Induction on n : (internal) memories

```

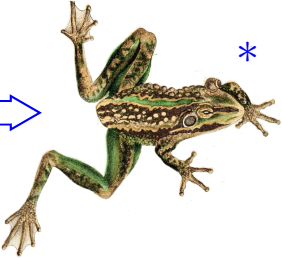
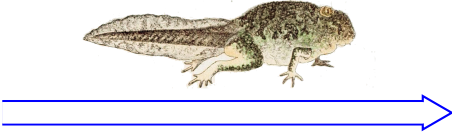
Inductive memory (A: Type): Type := mk_memory {
  mm_values : PM.t A;
  mm_instances : PM.t (memory A)
}.
  
```

Definition memory := memory (stream const).



$\exists M, \text{msem_node } G \text{ f xs } M \text{ ys}$

$\text{sem_node } G \text{ f xs ys}$



$\text{stmt_eval (translate } G) \text{ memv env (r := f.step obj (ci)) (memv', env')}$



Inductive sem_equation (G: global)
: history → equation → Prop :=

| (SEqDef:)

$$\frac{\text{sem_var } H \ x \ xs \rightarrow \text{sem_caexp } H \ cae \ xs \rightarrow}{\text{sem_equation } G \ H \ (\text{EqDef } x \ cae)}$$
$$x = (ce)^{ck}$$

...

| (SEqFby:)

$$\frac{\text{sem_laexp } H \ lae \ ls \rightarrow \text{sem_var } H \ x \ xs \rightarrow \text{xs} = \text{fby } v0 \ ls \rightarrow}{\text{sem_equation } G \ H \ (\text{EqFby } x \ v0 \ lae)}$$
$$x = (v0 \ \text{fby} \ le)^{ck}$$



Inductive sem_equation (G: global)
 : history → equation → Prop :=

| (SEqDef):

$$\frac{\text{sem_var } H \ x \ xs \rightarrow \text{sem_caexp } H \ cae \ xs \rightarrow}{\text{sem_equation } G \ H \ (\text{EqDef } x \ cae)}$$

$$x = (ce)^{ck}$$

...

| (SEqFby):

$$\frac{\text{sem_laexp } H \ lae \ ls \rightarrow \text{sem_var } H \ x \ xs \rightarrow \text{xs} = \text{fby } v0 \ ls \rightarrow}{\text{sem_equation } G \ H \ (\text{EqFby } x \ v0 \ lae)}$$

$$x = (v0 \ \text{fby} \ le)^{ck}$$

Inductive msem_equation G
 : history → memory → equation
 → Prop :=

| SEqDef:

$$\frac{\text{sem_var } H \ x \ xs \rightarrow \text{sem_caexp } H \ cae \ xs \rightarrow}{\text{msem_equation } G \ H \ M \ (\text{EqDef } x \ cae)}$$

...

| SEqFby:

$$\text{mfind_mem } x \ M = \text{Some } ms \rightarrow$$

$$ms \ 0 = v0 \rightarrow$$

$$\text{sem_laexp } H \ lae \ ls \rightarrow$$

$$\text{sem_var } H \ x \ xS \rightarrow$$

$$(\forall n,$$

$$\text{match } ls \ n \ \text{with}$$

$$| \ \text{absent} \Rightarrow ms \ (S \ n) = ms \ n$$

$$\quad \wedge \ xS \ n = \text{absent}$$

$$| \ \text{present } v \Rightarrow ms \ (S \ n) = v$$

$$\quad \wedge \ xS \ n = \text{present } (ms \ n)$$

$$\text{end}) \rightarrow$$

$$\text{msem_equation } G \ H \ M \ (\text{EqFby } x \ v0 \ lae)$$



n	0	1	2	3	4	5	6	...	base
ck	F	F	T	F	T	T	F	...	base
x = n when (ck = T)			3		5	6		...	base on (ck = T)
y = 0 fby x			0		3	5		...	base on (ck = T)
y _M = 0 mby x	0	0	0	3	3	5	6	...	base

...

| SEqFby:

mfind_mem x M = Some ms →

ms 0 = v0 →

sem_laexp H lae ls →

sem_var H x xS →

(∀ n,

 match ls n with

 | absent ⇒ ms (S n) = ms n

 ∧ xs n = absent

 | present v ⇒ ms (S n) = v

 ∧ xs n = present (ms n)

 end) →

msem_equation G H M (EqFby x v0 lae)

| SEqFby:

sem_laexp H lae ls →

sem_var H x xs →

xs = fby v0 ls →

sem_equation G H (EqFby x v0 lae)



n	0	1	2	3	4	5	6	...	base
ck	F	F	T	F	T	T	F	...	base
x = n when (ck = T)			3		5	6		...	base on (ck = T)
y = 0 fby x			0		3	5		...	base on (ck = T)
y _M = 0 mby x	0	0	0	3	3	5	6	...	base

...

| SEqFby:

mfind_mem x M = Some ms →

ms 0 = v0 →

sem_laexp H lae ls →

sem_var H x xS →

(∀ n,

match ls n with

| absent ⇒ ms (S n) = ms n
 ∧ xs n = absent

| present v ⇒ ms (S n) = v

∧ xs n = present (ms n)

end) →

msem_equation G H M (EqFby x v0 lae)

| SEqFby:

sem_laexp H lae ls →

sem_var H x xs →

xs = fby v0 ls →

sem_equation G H (EqFby x v0 lae)



n	0	1	2	3	4	5	6	...	base
ck	F	F	T	F	T	T	F	...	base
x = n when (ck = T)			3		5	6		...	base on (ck = T)
y = 0 fby x			0		3	5		...	base on (ck = T)
y _M = 0 mby x	0	0	0	3	3	5	6	...	base

...

| SEqFby:

mfind_mem x M = Some ms →

ms 0 = v0 →

sem_laexp H lae ls →

sem_var H x xS →

(∀ n,

match ls n with

| absent ⇒ ms (S n) = ms n
 ∧ xs n = absent

| present v ⇒ ms (S n) = v

∧ xs n = present (ms n)

end) →

msem_equation G H M (EqFby x v0 lae) / 32

| SEqFby:

sem_laexp H lae ls →

sem_var H x xs →

xs = fby v0 ls →

sem_equation G H (EqFby x v0 lae)



n	0	1	2	3	4	5	6	...	base
ck	F	F	T	F	T	T	F	...	base
x = n when (ck = T)			3		5	6		...	base on (ck = T)
y = 0 fby x			0		3	5		...	base on (ck = T)
y _M = 0 mby x	0	0	0	3	3	5	6	...	base

Red arrows indicate the mapping from the 'base' row to the 'base on (ck = T)' row, and from the 'base on (ck = T)' row to the 'base on (ck = T)' row.

SEqFby:

sem_laexp H lae ls →
 sem_var H x xs →
 xs = fby v0 ls →
sem_equation G H (EqFby x v0 lae)

SEqFby:

mfind_mem x M = Some ms →

ms 0 = v0 →

sem_laexp H lae ls →

sem_var H x xS →

(∀ n,

match ls n with

| absent ⇒ ms (S n) = ms n

∧ xs n = absent

| present v ⇒ ms (S n) = v

∧ xs n = present (ms n)

end) →

msem_equation G H M (EqFby x v0 lae) / 32



n	0	1	2	3	4	5	6	...	base
ck	F	F	T	F	T	T	F	...	base
x = n when (ck = T)			3		5	6		...	base on (ck = T)
y = 0 fby x			0		3	5		...	base on (ck = T)
y _M = 0 mby x	0	0	0	3	3	5	6	...	base

...

| SEqFby:

mfind_mem x M = Some ms →

ms 0 = v0 →

sem_laexp H lae ls →

sem_var H x xS →

(∀ n,

 match ls n with

 | absent ⇒ ms (S n) = ms n
 ∧ xs n = absent

 | present v ⇒ ms (S n) = v

 ∧ xs n = present (ms n)

 end) →

msem_equation G H M (EqFby x v0 lae)

| SEqFby:

sem_laexp H lae ls →

sem_var H x xs →

xs = fby v0 ls →

sem_equation G H (EqFby x v0 lae)



n	0	1	2	3	4	5	6	...	base
ck	F	F	T	F	T	T	F	...	base
x = n when (ck = T)			3		5	6		...	base on (ck = T)
y = 0 fby x			0		3	5		...	base on (ck = T)
y _M = 0 mby x	0	0	0	3	3	5	6	...	base

...

| SEqFby:

mfind_mem x M = Some ms →

ms 0 = v0 →

sem_laexp H lae ls →

sem_var H x xS →

(∀ n,

 match ls n with

 | absent ⇒ ms (S n) = ms n

 ∧ xs n = absent

 | present v ⇒ ms (S n) = v

 ∧ xs n = present (ms n)

end) →

msem_equation G H M (EqFby x v0 lae) / 32

| SEqFby:

sem_laexp H lae ls →

sem_var H x xs →

xs = fby v0 ls →

sem_equation G H (EqFby x v0 lae)



n	0	1	2	3	4	5	6	...	base
ck	F	F	T	F	T	T	F	...	base
x = n when (ck = T)			3		5	6		...	base on (ck = T)
y = 0 fby x			0		3	5		...	base on (ck = T)
y _M = 0 mby x	0	0	0	3	3	5	6	...	base

...

| SEqFby:

mfind_mem x M = Some ms →

ms 0 = v0 →

sem_laexp H lae ls →

sem_var H x xS →

(∀ n,

 match ls n with

 | absent ⇒ ms (S n) = ms n

 ∧ xs n = absent

 | present v ⇒ ms (S n) = v

 ∧ xs n = present (ms n)

end) →

msem_equation G H M (EqFby x v0 lae) / 32

| SEqFby:

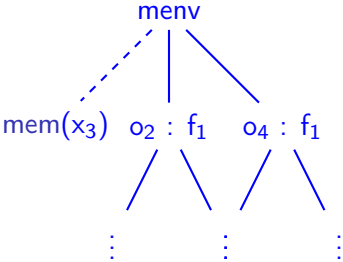
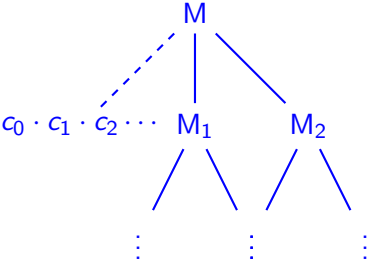
sem_laexp H lae ls →

sem_var H x xs →

xs = fby v0 ls →

sem_equation G H (EqFby x v0 lae)

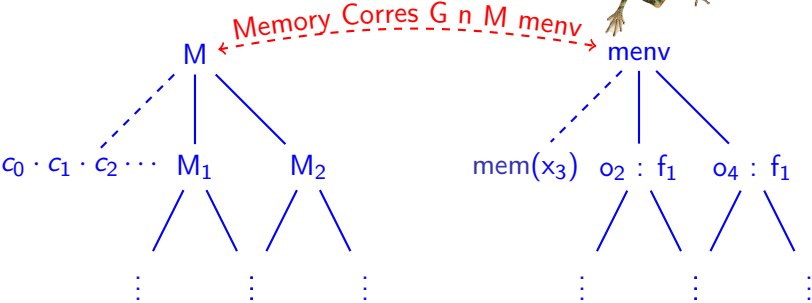
Memory Correspondence



```

Inductive Memory_Corres (G: global) (n: nat) :
  ident → memory → heap → Prop :=
| MemC:
  find_node f G = Some(mk_node f i o eqs) →
  Forall (Memory_Corres_eq G n M memv) eqs →
  Memory_Corres G n f M memv
  
```

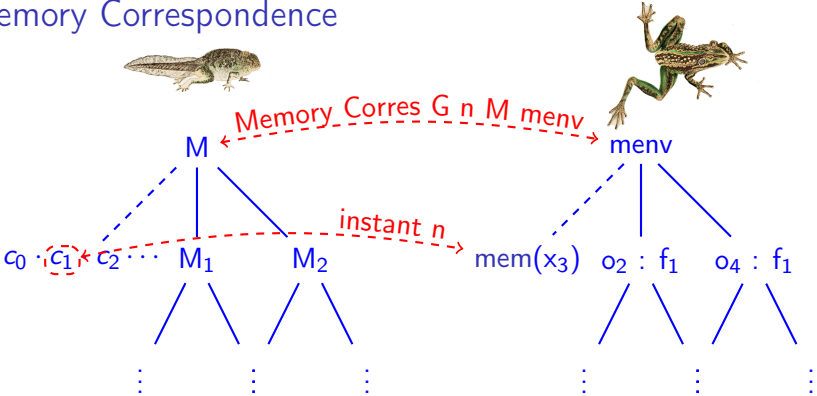
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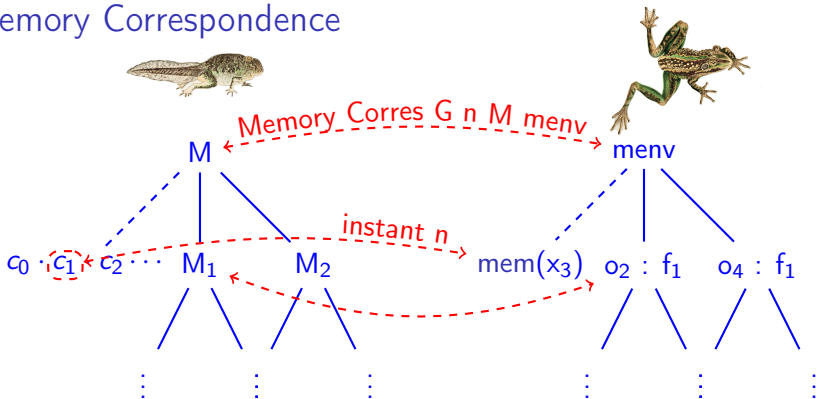

Memory Correspondence



Inductive Memory_Corres_eq (G: global) (n: nat) :
 memory → heap → equation → Prop :=

...
 | MemC_EqFby:
 (∀ ms, mfind_mem x M = Some ms
 → mfind_mem x menv = Some (ms n))
 → Memory_Corres_eq G n M menv (EqFby x v0 lae).

Memory Correspondence



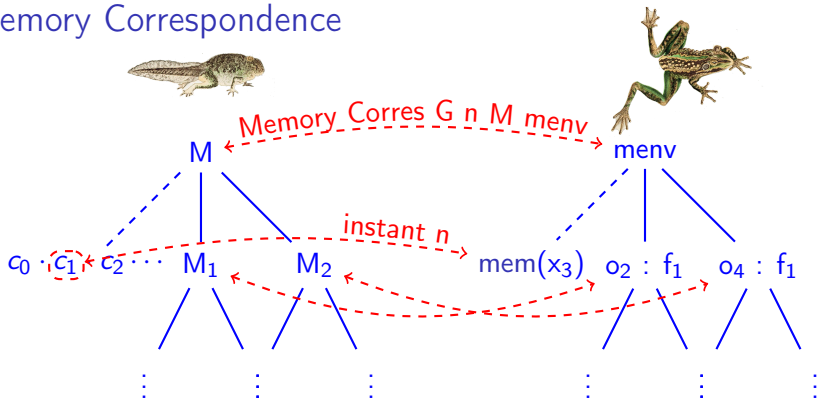
Inductive Memory_Corres_eq (G: global) (n: nat) :
 memory → heap → equation → Prop :=

...

| MemC_EqApp:

(\forall Mo, mfind_inst x M = Some Mo →
 (\exists omenv, mfind_inst x memv = Some omenv
 \wedge Memory_Corres G n f Mo omenv))
 → Memory_Corres_eq G n M memv (EqApp x f lae)

Memory Correspondence



Inductive Memory_Corres_eq (G: global) (n: nat) :
 memory \rightarrow heap \rightarrow equation \rightarrow Prop :=

...

| MemC_EqApp:

(\forall Mo, mfind_inst x M = Some Mo \rightarrow
 (\exists omenv, mfind_inst x memv = Some omenv
 \wedge Memory_Corres G n f Mo omenv))
 \rightarrow Memory_Corres_eq G n M memv (EqApp x f lae)

Lemma is_step_correct:

Forall (msem_equation G H M) alleqs
(\exists oeqs, alleqs = oeqs ++ eqs)

Welldef_global G \rightarrow

(\forall c, sem_var_instant (restr H n) input (present c)
 \leftrightarrow PM.find input env = Some c) \rightarrow
 \rightarrow Is_defined_in input eqs \rightarrow

Is_well_sch mems input eqs \rightarrow

(* hypothesis for earlier nodes... *)

Forall (Memory_Corres_eq G n M memv) alleqs \rightarrow

(\exists memv' env',
stmt_eval (translate G) memv env
(translate_eqns mems eqs) (memv', env'))
 \wedge (\forall x, Is_variable_in x eqs \rightarrow
 \forall c, sem_var_instant (restr H n) x (present c)
 \leftrightarrow PM.find x env' = Some c)
 \wedge Forall (Memory_Corres_eq G (S n) M memv') eqs).

induction n

└ induction G

└ induction eqs

└ case: $x = (ce)^{ck}$

└ case: present

└ case: absent

└ case: $x = (f e)^{ck}$

└ case: present

└ case: absent

└ case: $x = (k fby e)^{ck}$

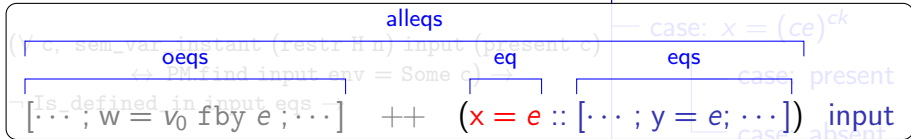
└ case: present

└ case: absent

Lemma is_step_correct:

Forall (msem_equation G H M) alleqs
(\exists oeqs, alleqs = oeqs ++ eqs)

Welldef_global G \rightarrow



Is_well_sch mems input eqs \rightarrow

(* hypothesis for earlier nodes... *)

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 \wedge Forall (Memory_Corres_eq G (S n) M memv') eqs).

induction n

└ induction G

└ induction eqs

└ case: $x = (ce)^{ck}$

└ case: present

└ case: absent

└ case: $x = (f e)^{ck}$

└ case: present

└ case: absent

└ case: $x = (k fby e)^{ck}$

└ case: present

└ case: absent

Outline

A simple program in Lustre

Verifying Lustre compilation in Coq

Dataflow language: syntax and semantics

Imperative language: syntax and semantics

Relating the Dataflow and Imperative models

Optimization of control structures

Conclusion

Fusion of control structures [Biernacki et al. (2008): "Clock-directed modular code generation for synchronous data-flow languages"]

```
step(delta: int, sec: bool)
```

```
  returns (v: int) {
```

```
    var r, t : int;
```

```
    r := count.step o1 (0, delta, false);
```

```
    if sec {
```

```
      t := count.step o2 (1, 1, false)
```

```
    };
```

```
    if sec {
```

```
      v := r / t
```

```
    } else {
```

```
      v := mem(w)
```

```
    };
```

```
    mem(w) := v
```

```
  }
```

```
step(delta: int, sec: bool)
```

```
  returns (v: int) {
```

```
    var r, t : int;
```

```
    r := count.step o1 (0, delta, false);
```

```
    if sec {
```

```
      t := count.step o2 (1, 1, false);
```

```
      v := r / t
```

```
    } else {
```

```
      v := mem(w)
```

```
    };
```

```
    mem(w) := v
```

```
  }
```


- Generate control for each equation (simpler to implement and prove).
- Afterward fuse control structures together.
- Effective if scheduler places similarly clocked equations together.

Fusion of control structures: requires invariant

```
if e {s1} else {s2};  
if e {t1} else {t2}   if e {s1; t1} else {s2; t2};
```


Fusion of control structures: requires invariant

`if e {s1} else {s2};`
`if e {t1} else {t2}`  `if e {s1; t1} else {s2; t2};`

`if x {x := false} else {x := true};`
`if x {t1} else {t2}` 

Fusion of control structures: requires invariant

$\text{if } e \{s_1\} \text{ else } \{s_2\};$
 $\text{if } e \{t_1\} \text{ else } \{t_2\}$ \implies $\text{if } e \{s_1; t_1\} \text{ else } \{s_2; t_2\};$

$\text{if } x \{x := \text{false}\} \text{ else } \{x := \text{true}\};$
 $\text{if } x \{t_1\} \text{ else } \{t_2\}$ \times

$$\frac{\text{fusible}(s_1) \quad \text{fusible}(s_2) \quad \forall x \in \text{free}(e), \neg \text{maywrite } x \ s_1 \wedge \neg \text{maywrite } x \ s_2}{\text{fusible}(\text{if } e \{s_1\} \text{ else } \{s_2\})}$$

$$\frac{\text{fusible}(s_1) \quad \text{fusible}(s_2)}{\text{fusible}(s_1; s_2)}$$

...

Fusion of control structures: implementation

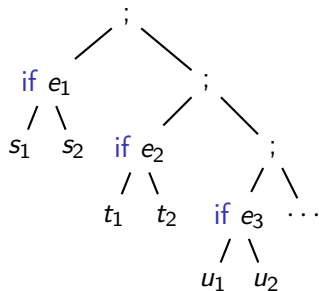
$$\text{fuse} \left(\begin{array}{c} ; \\ / \quad \backslash \\ s \quad t \end{array} \right) = \text{fuse}'(s, t)$$
$$\text{fuse}(s) = s$$

$$\text{fuse}' \left(s, \begin{array}{c} ; \\ / \quad \backslash \\ t_1 \quad t_2 \end{array} \right) = \text{fuse}'(\text{zip}(s, t_1), t_2)$$
$$\text{fuse}'(s, t) = \text{zip}(s, t)$$

$$\text{zip} \left(\begin{array}{c} \text{if } e \\ / \quad \backslash \\ s_1 \quad s_2 \end{array}, \begin{array}{c} \text{if } e \\ / \quad \backslash \\ t_1 \quad t_2 \end{array} \right) = \begin{array}{c} \text{if } e \\ / \quad \backslash \\ \text{zip}(s_1, t_1) \quad \text{zip}(s_2, t_2) \end{array}$$

$$\text{zip} \left(\begin{array}{c} ; \\ / \quad \backslash \\ s_1 \quad s_2 \end{array}, t \right) = \begin{array}{c} ; \\ / \quad \backslash \\ s_1 \quad \text{zip}(s_2, t) \end{array}$$

$$\text{zip}(s, t) = \begin{array}{c} ; \\ / \quad \backslash \\ s \quad t \end{array}$$

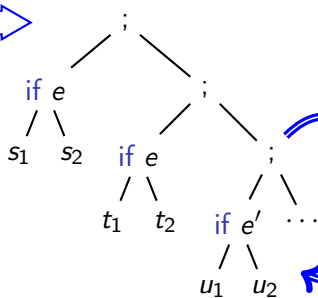


Fusion of control structures: correctness

eqs

$\xrightarrow{\text{translate_eqns}}$

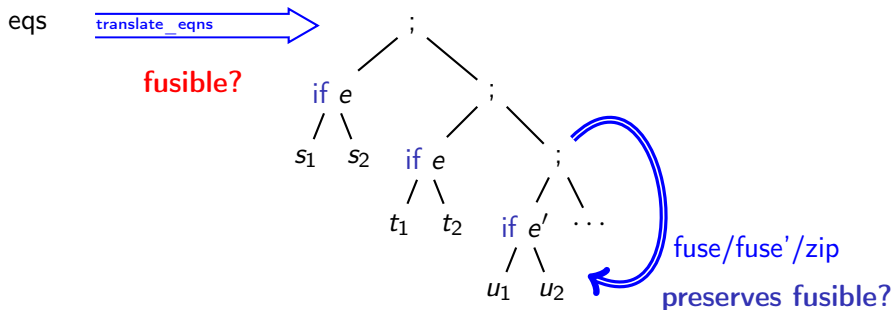
fusible?



fuse/fuse'/zip

preserves fusible?

Fusion of control structures: correctness

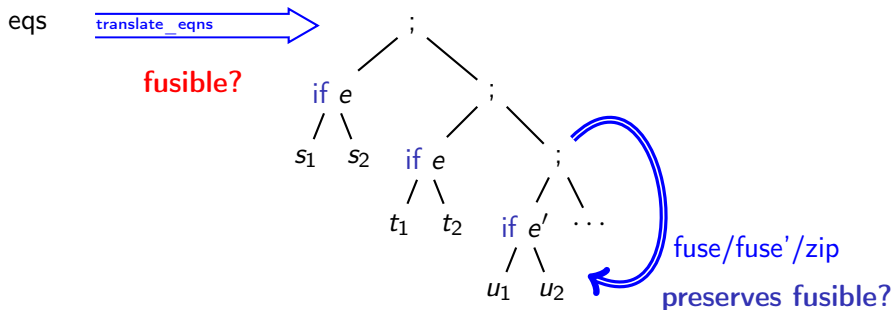


$x = (\text{merge } b \ e1 \ e2)^{\text{base on } ck}$

```
if ck {  
  if b {  
    x := e1  
  } else {  
    x := e2  
  }  
}
```

- In a well scheduled dataflow program it is not possible to read x before writing it.
- Compiling $x = (ce)^{ck}$ and $x = (fle)^{ck}$ gives **fusible** imperative code.

Fusion of control structures: correctness

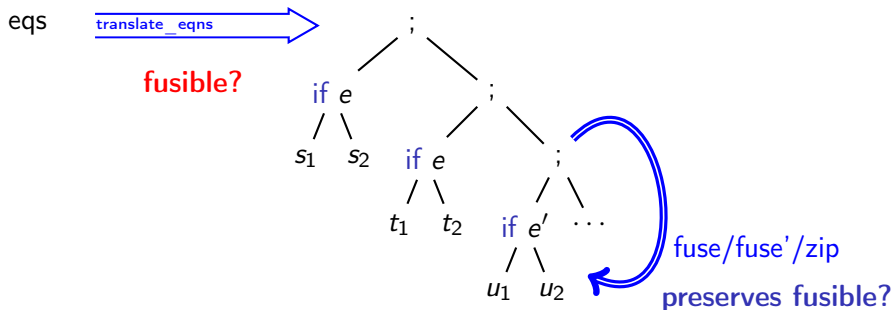


$x = (0 \text{ fby } (x + 1))$ base on ck

```
if ck {  
  mem(x) := mem(x) + 1  
}
```

- But for **fby** equations, we must read x before writing it.
- A different invariant?
Once we write x , we never read it again.
Trickier to express. Trickier to work with.

Fusion of control structures: correctness



$y = (\text{true when } x)^{\text{base on } x}$
 $x = (\text{true fby } y)^{\text{base on } x}$

```

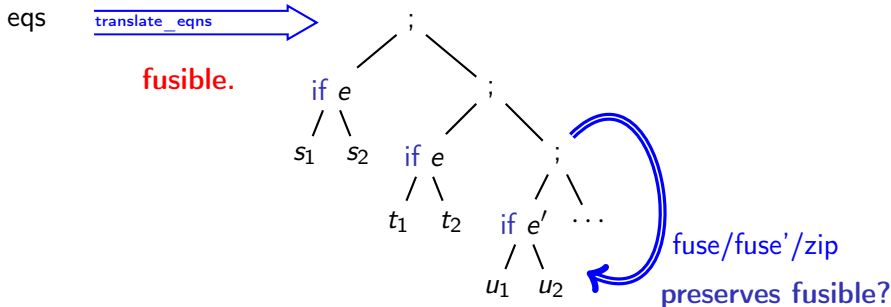
if mem(x) {
  y := true
}
if mem(x) {
  mem(x) := y
}
    
```

- Happily, such programs are not well clocked.

$$\frac{C \vdash \text{true} :: \text{base} \quad C \vdash x :: \text{base}}{C \vdash \text{true when } x :: \text{base on } (x = T)}$$

$$C \vdash x :: \text{base on } (x = T)$$

Fusion of control structures: correctness



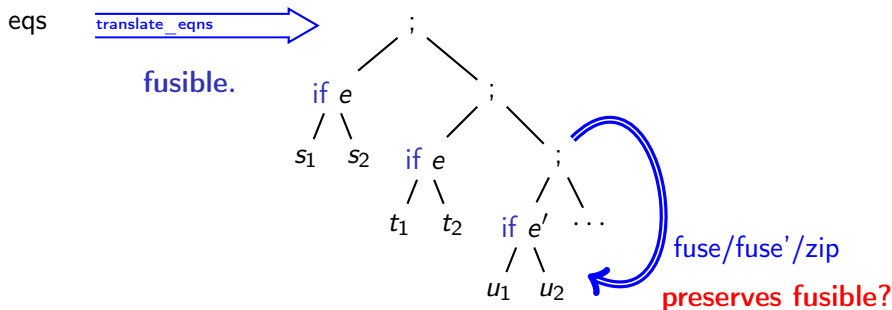
$y = (\text{true when } x)^{\text{base on } x}$
 $x = (\text{true fby } y)^{\text{base on } x}$

```

if mem(x) {
  y := true
}
if mem(x) {
  mem(x) := y
}
    
```

- Happily, such programs are not well clocked.
- Show that a variable x is never free in its own clock in a well clocked program:
 $C \not\vdash x :: \text{base on } \dots \text{ on } x \text{ on } \dots$
- Compiling $x = (v0 \text{ fby } le)^{\text{ck}}$ also gives **fusible** imperative code.

Fusion of control structures: correctness

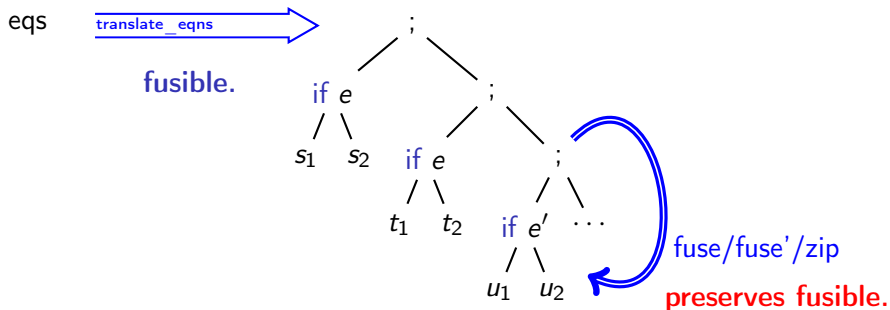


- Define $s_1 \approx_{eval} s_2$

Definition `stmt_eval_eq s1 s2: Prop :=`

\forall prog memv env memv' env',
stmt_eval prog memv env s1 (memv', env')
 \leftrightarrow
stmt_eval prog memv env s2 (memv', env').

Fusion of control structures: correctness



- Define $s_1 \approx_{eval} s_2$
- Define $s_1 \approx_{fuse} s_2$ as $s_1 \approx_{eval} s_2 \wedge \text{fusible}(s_1) \wedge \text{fusible}(s_2)$
- Show congruence ('Proper' instances) for ;/fuse/fuse'/zip.

- Rewrite until
$$\frac{\text{fusible}(s)}{\text{fuse}(s) \approx_{eval} s}$$

Conclusion

Preliminary results

- Semantics based on $(\text{nat} \rightarrow \text{value})$.
- Showed correctness of imperative code generation in Coq.
- Showed correctness of if/then/else fusion in Coq.






Ongoing work

- Well-typed, Well-clocked, Causal \rightarrow `sem_node G f xs ys`.
- Connection to CompCert Clight.
- Working tool-chain:
 - Verified parser generator.
 - Incorporation of scheduling and normalization.

Longer term aims

- Treat more sophisticated language features.
- Verify synchronous models in Coq and generate correct code.

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